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APPLICANTS

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**** CONTINUING DATA ******* *VERIFIED*

This application is a CON of 10/083,341 02/27/2002 PAT 6,708,322

**** FOREIGN APPLICATIONS ******* *VERIFIED*

JAPAN 2001-170636 06/06/2001

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 14	TOTAL CLAIMS 5	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance				
Verified and Acknowledged <i>AMT</i> Examiner's Signature Initials				

ADDRESS

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TITLE

Design method and apparatus for a semiconductor integrated circuit comprising checkers verifying the interface between circuit blocks

FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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